

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

Claims 1-9 (canceled).

Claim 10 (currently amended): A multilayer capacitor comprising:

a main body having first and second main surfaces and four side surfaces connecting the first and second main surfaces to each other, a plurality of dielectric layers, at least one pair of first and second internal electrodes between the dielectric layers and opposed to each other so as to generate an electrostatic capacitance; and

first and second external terminal electrodes arranged on an external surface of the main body so as to be electrically connected to the first and second internal electrodes, respectively; wherein

each of the first and second internal electrodes has a capacitance generating portion arranged to generate the electrostatic capacitance, a terminal connecting portion connected to the external terminal electrode, and an extended portion connecting the capacitance generating portion to the terminal connecting portion; and

~~the extended portion of at least one of the internal electrodes is curved in the direction of its thickness~~the extended portion of at least one of the internal electrodes is narrower than the capacitance generating portion and the terminal connecting portion;

the multilayer capacitor further comprises at least one dummy electrode arranged so as to be layered on the terminal connecting portion of the at least one of the internal electrodes; and

an end of the at least one dummy electrode extends toward a position opposed to a lengthwise intermediate portion of the extended portion of the at least one of the terminal electrodes so as to cause the extended portion of the at least one of the

internal electrodes to be curved in a direction of its thickness at a position opposed to the end of the at least one dummy electrode.

Claim 11 (canceled).

Claim 12 (currently amended): The multilayer capacitor according to Claim ~~11~~10, wherein the at least one dummy electrode includes at least one dummy electrode arranged below the first and second internal electrodes.

Claim 13 (previously presented): The multilayer capacitor according to Claim 12, wherein the at least one dummy electrode further includes at least one dummy electrode arranged above the first and second internal electrodes.

Claim 14 (canceled).

Claim 15 (previously presented): The multilayer capacitor according to Claim 10, wherein the extended portion that is curved in the direction of its thickness is thinner than the capacitance generating portion and the terminal connecting portion.

Claim 16 (previously presented): The multilayer capacitor according to Claim 10, wherein at least one pair of the internal electrodes is adjacent the first main surface of the main body, the first main surface opposing a mounting surface for the multilayer capacitor.

Claim 17 (previously presented): The multilayer capacitor according to Claim 16, wherein all of the first and second external terminal electrodes are adjacent the first main surface.

Claim 18 (previously presented): The multilayer capacitor according to Claim 16, further comprising another pair of the first and second internal electrodes adjacent the second main surface of the main body.

Claim 19 (previously presented): The multilayer capacitor according to Claim 10, wherein the first and second external terminal electrodes are alternately arranged along one of the side surfaces of the main body.

Claim 20 (previously presented): A method of manufacturing a multilayer capacitor including a main body having first and second main surfaces and four side surfaces connecting the main surfaces to each other, a plurality of dielectric layers, at least one pair of first and second internal electrodes between the dielectric layers and opposed to each other so as to generate an electrostatic capacitance; first and second external terminal electrodes arranged on an external surface of the main body so as to be electrically connected to the first and second internal electrodes, respectively; wherein each of the first and second internal electrodes has a capacitance generating portion functioning so as to generate the electrostatic capacitance, a terminal connecting portion connected to the external terminal electrode, and an extended portion connecting the capacitance generating portion to the terminal connecting portion; and the extended portion of at least one of the internal electrodes is curved in the direction of its thickness, the method comprising steps of:

- preparing a plurality of ceramic green sheets, which serve as the dielectric layers;

- forming an internal electrode on at least one of the ceramic green sheets;

- forming a dummy electrode on at least one ceramic green sheet so as to overlap with the terminal connecting portion of the internal electrode;

- layering and pressing the plurality of ceramic green sheets in order to produce the main body in an unfired state, including a step of pressing a portion of the ceramic

green sheets provided between the capacitance generating portions of the internal electrodes and between the terminal connecting portion and the dummy electrode so as to flex toward the extended portion of the internal electrode to curve the extended portion in the direction of its thickness; and

firing the main body.

Claim 21 (previously presented): The method of manufacturing the multilayer capacitor according to Claim 20, wherein

the step of forming the dummy electrode includes a step of forming the dummy electrode on a ceramic green sheet having no internal electrode formed thereon; and the step of layering and pressing the ceramic green sheets further includes:

a step of layering and preliminarily pressing the ceramic green sheet having the dummy electrode formed thereon but having no internal electrode formed thereon to curve the inner edges of the dummy electrode in a layering direction; and

a step of layering and preliminarily pressing the ceramic green sheet having the internal electrode formed thereon to curve the extended portion in the direction of its thickness along the curvature of an inner edge of the dummy electrode.

Claim 22 (previously presented): The method of manufacturing the multilayer capacitor, according to Claim 21, wherein the step of forming the dummy electrode further includes a step of forming the dummy electrode on a ceramic green sheet having the internal electrode formed thereon.